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(54) Quarternary collector InAlAs-InGaAlAs heterojunction bipolar transistor

(57) An InAlAs/InGaAlAs heterojunction bipolar transistor that includes a constant quaternary InGaAlAs collector layer. Graded InGaAlAs collector layers are provided on each side of the quaternary collector layer to minimize transitions through the constant collector

layer. The InAlAs/InGaAlAs HBT may also include one or more of a graded InGaAlAs emitter-base transition region, a graded-doping InGaAs base layer, and a graded-composition InGaAlAs base layer.

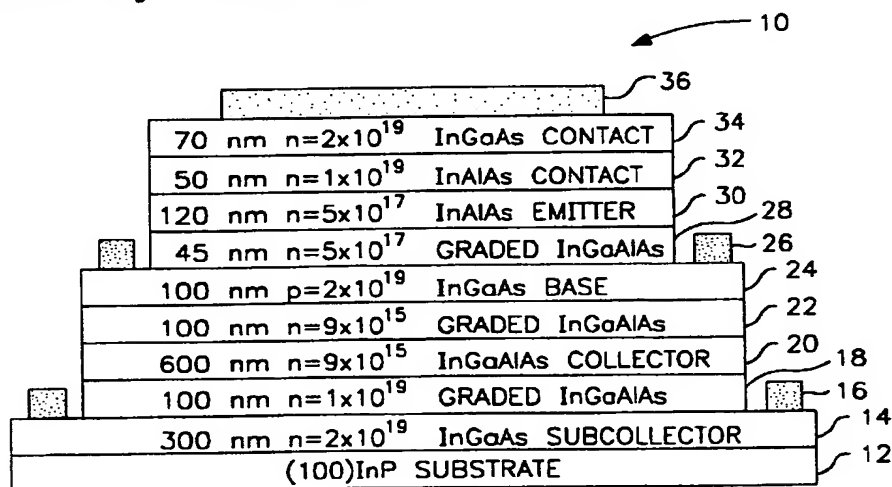


FIG. 1

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## Description

### BACKGROUND OF THE INVENTION

#### 1. Field Of The Invention

This invention relates generally to a heterojunction bipolar transistor and, more particularly, to an InAlAs/InGaAs heterojunction bipolar transistor that includes a constant InGaAlAs quaternary collector layer, a graded-composition InGaAlAs emitter-base transition region, a graded-doping InGaAs base layer and/or a graded-composition InGaAlAs base layer.

#### 2. Discussion of the Related Art

Because of certain known device performance advantages, heterojunction bipolar transistors (HBTs) are desirable for many applications, especially high power and high frequency microwave applications. Early HBTs were generally gallium arsenide/aluminum gallium arsenide (GaAs/AlGaAs) HBTs. As device performance demands increased, GaAs/AlGaAs HBTs were limited in their ability to provide high enough frequency performance, acceptable cut-off frequency ( $f_T$ ) and high enough gain for amplifying purposes. In order to overcome these limitations, indium gallium arsenide/indium aluminum arsenide (InGaAs/InAlAs) HBTs that were matched to indium phosphide (InP) substrates were devised. The advantages of the InAlAs/InGaAs HBTs over the GaAs/AlGaAs HBTs included higher electron saturation velocity and mobility, lower turn-on voltages, and larger bandgap discontinuity that provided high frequency performance advantages. However, original InAlAs/InGaAs HBTs suffered from a low collector breakdown voltage ( $BV_{CEO}$ ) and high output conductance that limited their use in high frequency microwave applications. In a typical example, an InGaAs collector had a bandgap of 0.75eV as compared with a GaAs collector having a bandgap of 1.45eV in an AlGaAs/GaAs HBT. Further, InGaAs/InAlAs HBTs experience a decrease in collector breakdown voltage  $BV_{CEO}$  with increasing temperature.

A number of different concepts have been put forward in order to provide an acceptable collector breakdown voltage  $BV_{CEO}$  and output conductance for InAlAs/InGaAs HBTs. For example, InAlAs/InGaAs HBTs have been developed that have collector layers of InP. See for example Stanchina, W.E., "Status and Potential of AlInAs/GaInAs/InP HBT ICs," Proc. Fourth Int. Conf. Indium Phosphide and Related Materials, pp. 434-447 (1992) and Nottenburg, R.N. et al, "High-speed InGaAs(P)/InP double-heterostructure bipolar transistors," IEEE Electron Device Lett., vol. EDL-8, pp. 282-284 (1983). Additionally, InAlAs/InGaAs HBTs having InAlAs collector layers have also been provided. See for example, Farley, C.W. et al. "Performance tradeoffs in AlInAs/GaInAs single- and double-heterojunction NPN heterojunction bipolar transistors," J. Vac. Sci. Technol.,

vol. B10, pp. 1023-1025 (1992). One reference has reported use of a graded InGaAlAs collector layer in an InAlAs/InGaAs HBT. See Vleck, J.C. et al. "Multiply-graded InGaAs heterojunction bipolar transistors," Electron Lett., vol. 27, pp. 1213-1215 (1991). Further, composite InGaAs/InP collectors in HBTs have also been reported. The use of InP or composite InGaAs/InP collectors has been limited, however, because the growth of phosphorous based compounds is not compatible with most solid-source molecular beam epitaxial (MBE) systems. U.S. Patent No. 5,150,185 issued September 22, 1992 to Yamada discloses an HBT semiconductor device that appears to include a quaternary  $(\text{InGaAs})_{0.4}(\text{InAlAs})_{0.6}$  collector layer positioned between two n-type  $(\text{InGaAs})_{1-x}(\text{InAlAs})_x$  graded layers.

Although recent semiconductor HBT devices have had some success in providing low collector breakdown voltages and high output conductances, while at the same time providing high frequency performance, acceptable cut-off frequencies and high gain, other device criteria are also important for realizing increased device performance. For example, it is important that HBT devices have high base electron transit times so as to increase the switching speed and frequency of the device. U.S. Patent Application Serial No. 07/876,199, filed April 30, 1992, titled "Heterojunction Bipolar Transistor with Graded Base Doping" and assigned to the assignee of the instant application, discloses a heterojunction bipolar transistor that has graded base doping so as to increase the base transit time of the device. However, this is a GaAs/AlGaAs HBT device, and thus suffers from its ability to provide high frequency performance, etc., as discussed above. Also, U.S. Patent No. 5,284,783 issued February 8, 1994 to Ishikawa et al. discloses a heterojunction bipolar transistor that includes a graded InGaAlAs base layer and a graded InGaAlAs emitter-base transition layer.

What is needed is a high performance HBT that does not suffer from a low collector breakdown voltage or high output conductance, and offers other device performance capabilities, such as increased base transit times, for high power applications. It is therefore an object of the present invention to provide such a device.

#### SUMMARY OF THE INVENTION

In accordance with the teachings of the present invention, an InAlAs/InGaAlAs HBT is disclosed that includes a constant quaternary InGaAlAs collector layer. The constant quaternary InGaAlAs collector layer allows for high frequency performance as well as high collector breakdown voltages and output conductances. Graded InGaAlAs collector layers are positioned on each side of the constant InGaAlAs collector layer to minimize transitions through the constant collector layer. The InAlAs/InGaAlAs HBT may also include one or more of a graded InGaAlAs emitter-base transition region, a graded-doping InGaAs base layer, and a graded-composition InGaAlAs base layer. The graded

emitter-base transition region reduces the transition between the emitter and base layers of the HBT and the graded-doping and graded-composition base layer provide for increased base transit times.

Additional objects, advantages and features of the present invention will become apparent from the following description and appended claims, taken in conjunction with the accompanying drawings.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is a profile view of an InAlAs/InGaAlAs HBT including a quarternary InGaAlAs collector layer and a graded InGaAlAs emitter-base transition region according to an embodiment of the present invention;

Figure 2 is a profile view of an InAlAs/InGaAlAs HBT including a quarternary InGaAlAs collector layer and a graded doping InGaAs base layer according to an embodiment of the present invention;

Figure 3 is a profile view of an InAlAs/InGaAlAs HBT including a quarternary InGaAlAs collector layer and a graded-composition InGaAlAs base layer according to an embodiment of the present invention;

Figure 4 is a profile view of an InAlAs/InGaAlAs HBT including a quaternary InGaAlAs collector layer and a graded-doping and graded-composition InGaAlAs base layer according to an embodiment of the present invention;

Figure 5 is a graph of the Ga mole fraction content of each layer of the HBT of Figure 1;

Figure 6 is a graph of the Al mole fraction content of each layer of the HBT of Figure 1;

Figure 7 is a graph of the Ga mole fraction content of each layer of the HBT of Figure 3;

Figure 8 is a graph of the Al mole fraction content of each layer of the HBT of Figure 3;

Figure 9 is a bandgap diagram of the HBT of Figure 2;

Figure 10 is a bandgap diagram of the HBT of Figure 3;

Figures 11(a) and 11(b) show a graph of collector current ( $I_c$ ) in the vertical axis versus collector-emitter voltage ( $V_{CE}$ ) in the horizontal axis depicting breakdown voltage for an InGaAs collector and an InGaAlAs collector, respectively; and

Figure 12 is a graph of output conductance ( $g_o$ ) in the vertical axis and collector-emitter voltage ( $V_{CE}$ ) in the horizontal axis for an InGaAs collector and an InGaAlAs collector.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

The following discussion of the preferred embodiments concerning an InAlAs/InGaAlAs HBT semicon-

ductor device is merely exemplary in nature and is not intended to limit the invention or its applications or uses.

In accordance with the teachings of the present invention, a constant quarternary InGaAlAs collector layer in an HBT is proposed rather than the known InGaAs collector layer which exhibits low collector breakdown voltages, or the known InAlAs collector layer which exhibits undesirable current blocking as a result of the InGaAs/InAlAs interface of the base and collector layers of the HBT. This satisfies the above described problem concerning low collector breakdown voltage and high output conductance generally present in known InAlAs/InGaAs HBTs. Further, the semiconductor device of the invention may include one or more of a graded-composition InGaAlAs base layer, a graded InGaAlAs emitter-base transition region, and a graded-doping InGaAs base layer.

Figure 1 shows a profile view of an HBT 10 according to an embodiment of the present invention. The HBT 10 is lattice matched to an InP substrate 12 where each layer is grown by a molecular beam epitaxy growth process well understood in the art. In one example, the device material was grown by solid-source MBE using cracked arsenic, where silicon was used for the n-type doping material and beryllium was used for the p-type doping material. Of course, other doping materials, other layer thicknesses, other layer structures, etc., than those set out below can be provided without departing from the scope of the invention. The different layers of the HBT 10 were appropriately patterned and etched by an applicable process that would be well understood to one skilled in the art.

An n-type  $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$  subcollector layer 14 doped to about  $2 \times 10^{19}$  atoms/cm<sup>3</sup> is grown on the substrate 12 to a thickness of about 300 nm. As is well understood in the art, the composition of the As in these types of HBTs is generally 50%, and the remaining components, i.e., the In, Ga, and/or Al make up the remaining 50% of the composition. A pair of collector contacts 16 are evaporated and patterned on the subcollector layer 14 after the other layers are appropriately etched in a process that is well understood in the art. An n-type InGaAlAs graded collector layer 18 doped to about  $1 \times 10^{19}$  atoms/cm<sup>3</sup> is grown on the subcollector layer 14 to a thickness of about 100 nm. In one embodiment, the graded layer 18 has a composition of  $\text{In}_{0.53}\text{Ga}_{0.46}\text{Al}_{0.01}\text{As}$  adjacent the subcollector layer 14, and is graded to have a composition of  $\text{In}_{0.53}\text{Ga}_{0.24}\text{Al}_{0.23}\text{As}$  at a surface of the graded layer 18 opposite to the subcollector layer 14. An n-type  $\text{In}_{0.53}\text{Ga}_{0.24}\text{Al}_{0.23}\text{As}$  quaternary collector layer 20 doped to about  $1 \times 10^{15}$  atoms/cm<sup>3</sup> is grown on the graded layer 18 to a thickness of about 600 nm. An n-type InGaAlAs graded collector layer 22 doped to about  $9 \times 10^{15}$  atoms/cm<sup>3</sup> is grown on the collector layer 20 to a thickness of about 100 nm. The graded layer 22 has a composition of about  $\text{In}_{0.53}\text{Ga}_{0.24}\text{Al}_{0.23}\text{As}$  at a surface adjacent to the collector layer 20, and is graded to have a composition of  $\text{In}_{0.53}\text{Ga}_{0.46}\text{Al}_{0.01}\text{As}$  at a surface oppo-

site to the collector layer 20. The increased bandgap of the quaternary collector layer 20 results in improved device performance without the bandgap being so large as to impede normal device performance.

A p-type  $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$  base layer 24 doped to about  $4 \times 10^{19}$  atoms/cm<sup>3</sup> is grown on the graded layer 22 to a thickness of about 100 nm. A base contact 26 is evaporated and patterned on the base layer 24 after the upper emitter layers have been deposited and etched in a process that is well understood to one skilled in the art. An n-type  $\text{InGaAlAs}$  graded emitter-base transition layer 28 doped to about  $5 \times 10^{17}$  atoms/cm<sup>3</sup> is grown on the base layer 24 to a thickness of about 45 nm. The graded layer 28 has a composition of  $\text{In}_{0.53}\text{Ga}_{0.37}\text{Al}_{0.10}\text{As}$  at an interface between the graded layer 28 and the base layer 24, and is graded to have a composition of  $\text{In}_{0.52}\text{Ga}_{0.01}\text{Al}_{0.47}\text{As}$  at a surface of the graded layer 28 opposite to the base layer 24. An n-type  $\text{In}_{0.52}\text{Al}_{0.48}\text{As}$  emitter layer 30 doped to about  $5 \times 10^{17}$  atoms/cm<sup>3</sup> is grown on the graded layer 28 to a thickness of about 120 nm. An n-type  $\text{In}_{0.52}\text{Al}_{0.48}\text{As}$  emitter contact layer 32 doped to about  $1 \times 10^{19}$  atoms/cm<sup>3</sup> is grown on the emitter layer 30 to a thickness of about 50 nm. An n-type  $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$  emitter contact layer 34 doped to about  $2 \times 10^{19}$  atoms/cm<sup>3</sup> is grown on the emitter contact layer 32 to a thickness of approximately 70 nm. An emitter contact 36 is deposited and patterned on the emitter contact layer 34. Each of the thicknesses given above is by way of a non-limiting example in that other thicknesses for each layer within an operable range would be applicable within the scope of the invention.

Figure 2 shows a profile view of an HBT 10a according to another embodiment of the present invention. The HBT 10a is similar to the HBT 10 of Figure 1, however, the base layer 24 has been replaced with a graded-doped p-type  $\text{InGaAs}$  base layer 40. Because the remaining layers and contacts of the HBT 10a are the same as the corresponding layers and contacts of the HBT 10, these layers and contacts are labeled accordingly followed by the letter "a", and will not be specifically discussed. The base layer 40 is grown to a thickness of about 100 nm and has a doping concentration of approximately  $1 \times 10^{19}$  atoms/cm<sup>3</sup> at an interface between the base layer 40 and the graded  $\text{InGaAlAs}$  collector layer 22a. The thickness of 100 nm is by way of a non-limiting example in that any operable thickness can be used. The base layer 40 is graded such that the p-type doping concentration of the base layer 40 at the interface of the graded  $\text{InGaAlAs}$  emitter layer 28a is about  $4 \times 10^{19}$  atoms/cm<sup>3</sup>. The graded doping profile alters the fermi level within the base layer 40 such that the conduction band has a decreasing slope from the emitter side of the base layer 40 to the collector side of the base layer 40 that aids electron transport through the base layer 40. The graded doping concentration of the p-type dopant atoms creates a smooth grading in the conduction band of the base that provides for a higher base transit time so as to increase the switching

speed and frequency of the HBT 10a over the prior art HBTs.

Figure 3 shows a profile view of an HBT 10b according to another embodiment of the present invention. The HBT 10b is the same as the HBT 10 of Figure 1 except that the base layer 24 has been replaced with a graded-composition  $\text{InGaAlAs}$  base layer 42. Because the remaining layers and contacts of the HBT 10b are the same as the layers and contacts of the HBT 10, these layers and contacts will not be specifically discussed, and will be referenced by a corresponding reference numeral to that of the HBT 10 followed by the letter "b". In one embodiment, it is the composition of the GaAl constituents of the  $\text{InGaAlAs}$  base layer 42 that is graded and the In and As constituents remain constant. The base layer 42 is graded such that the composition of the base layer 42 is about  $\text{In}_{0.53}\text{Ga}_{0.46}\text{Al}_{0.01}\text{As}$  at an interface between the base layer 42 and the graded collector layer 22b, and the composition of the base layer 42 is  $\text{In}_{0.53}\text{Ga}_{0.24}\text{Al}_{0.25}\text{As}$  at an interface between the base layer 42 and the emitter-base transition layer 28b. In this manner, the base layer 42 is graded from a low bandgap at the collector side of the base layer 42 to a high bandgap at the emitter side of the base layer 42 so as to create a built-in electric field in the base layer 42 that aids electron transport through the base layer 42.

Figure 4 shows a profile view of a combination of the HBTs 10a and 10b as an HBT 10c. Specifically, the graded-doping of the  $\text{InGaAs}$  base layer 40 of the HBT 10a has been combined with the graded-composition of the  $\text{InGaAlAs}$  base layer 42 of the HBT 10b to arrive at a base layer 44 that includes both a graded-doping concentration and a graded-composition having the doping and composition of that of the layers 40 and 42 discussed above. Therefore, the base transit time of the base layer 44 is even greater as a result of the change in the bandgap of the base layer 44.

Figure 5 shows a graph of the Ga mole fraction content of each layer discussed above with reference to the HBT 10 of Figure 1. The Ga mole fraction content is given on the vertical axis and each layer of the HBT 10 is represented in nanometers (nm) from the top surface of the emitter contact layer 34 along the horizontal axis from left to right. In this configuration, a region 54 represents the subcollector layer 14 as having a Ga mole fraction content of 47%. A graded region 56 represents the graded collector layer 18 and a region 58 represents the collector layer 20. The region 56 has a Ga mole fraction content of about 47% adjacent the region 54 and is gradually graded so that the Ga mole fraction content is about 24% at an interface between the region 56 and the region 58. A graded region 60 represents the graded collector layer 22 and a region 62 represents the base layer 24. The graded region 60 has a Ga mole fraction content of about 24% at an interface with the region 58 and is gradually graded so that the mole fraction content of Ga at the interface of the region 60 and the region 62 is about 47%. A graded region 64 repre-

sents the graded emitter-base transition layer 28 and a region 66 represents the emitter contact layer 34. The graded region 64 has a Ga mole fraction content of about 47% at an interface between the region 64 and the region 62, and is graded to about 0% Ga at an interface between the region 64 and a region 66 that represents the emitter layer 30. A region 68 represents the combination of the emitter contact layers 32 and 34 as having a Ga content of 48%.

Figure 6 is a graph showing the Al mole fraction content for each layer of the HBT 10 of Figure 1. As above, the mole fraction content is given on the vertical axis and each layer of the HBT is represented in nanometers (nm) on the vertical axis. A graded region 72 represents the graded collector layer 18 and a region 74 represents the collector layer 20. The aluminum mole fraction content of the region 72 is about 23% at an interface between the graded region 72 and the region 74, and is gradually graded to about 0% Al at a region 76 representing the subcollector layer 14. Likewise, a graded region 78 represents the graded collector layer 22 and a region 80 represents the base layer 24. The aluminum mole fraction content of the graded region 78 is about 23% at an interface between the graded region 78 and the region 74, and is gradually graded to about 0% aluminum at the region 80. A graded region 82 represents the graded emitter-base transition region 28 and a region 84 represents the emitter layer 30. The graded region 82 has an aluminum mole fraction content of about 48% at an interface between the region 84 and the graded region 82, and is gradually graded to about 0% aluminum at an interface between the graded region 82 and the region 80.

Figure 7 shows a graph 52b that depicts the Ga mole fraction content in the layers of the HBT 10b of Figure 3. The graph of Figure 7 is substantially the same as the graph 52 of Figure 5, so therefore like regions of the graph 52b are labeled the same as the regions in the graph 52 followed by the letter "b". The difference between the graph 52b and the graph 52 is that the region 62 has been replaced with a graded region 88 to represent the graded-composition base layer 42. The graded region 88 has a Ga mole fraction content of about 47% at an interface between the graded region 60b and the graded region 88, and is graded so that the content of the Ga in the graded region 84 is about 24% at an interface between the graded region 84 and the graded region 64b.

Figure 8 shows a graph 70b of the aluminum mole fraction content of the HBT 10b of Figure 3. The graph 70b is almost identical to that of the graph 70 of Figure 6 and therefore like regions of the graph 70b to that of the graph 70 are labeled accordingly and followed by the letter "b". The difference between the graph 70b and the graph 70 is that a new region 90 has been added to represent the graded composition of the base layer 42. The region 90 has an aluminum mole fraction content of about 0% at an interface between the region 90 and the region 78b and an aluminum mole fraction content of

about 25% at an interface between the region 90 and the region 82b.

Figure 9 shows a bandgap diagram of the bandgaps of the different layers of the HBT 10a of Figure 2. Each of the different layers are shown being separated by a dotted vertical line. The energy between the conduction band ( $E_c$ ) and the valence band ( $E_v$ ) is given in electron volts (eV) for each layer that is not a graded composition layer. The substrate 12a is represented as a bandgap region 92. The subcollector layer 14a is represented as a bandgap region 94. The graded collector layer 18a is represented as a bandgap region 96. The collector layer 20 is represented as a bandgap region 98. The graded collector layer 22a is represented as a bandgap region 100. The graded-doping base layer 40 is represented as a bandgap region 102. The emitter-base transition layer 28a is represented as a bandgap region 104. The emitter layer 30a is represented as a bandgap region 106. The emitter contact layer 32a is represented as a bandgap region 108. And, the emitter contact layer 34a is represented as a bandgap region 110.

Figure 10 shows a bandgap diagram of the bandgaps of the different layers of the HBT 10b of Figure 3. The graded-composition based layer 42 is represented by a bandgap region 114. The other bandgap regions of Figure 10 are the same as the bandgap regions of Figure 9, and therefore, like bandgap regions to those of the bandgap regions of Figure 9 are represented by the same reference numeral followed by the letter "a". As is apparent, however, the bandgap energy level of the bandgap region 114 is 0.85eV adjacent to the bandgap region 104a, and gradually decreases to a bandgap energy level of 0.76eV at an interface between the bandgap region 114 and the bandgap region 100a. It is the gradual increase in the bandgap energy level across the bandgap region 114 that creates a built-in electric field that allows for an increase in the base-transit time of the electron in the HBT 10b.

Figures 11(a) and 11(b) show the collector current  $I_C$  in the vertical axis and the collector-emitter voltage  $V_{CE}$  in the horizontal axis that depict the breakdown voltage  $V_{BE}$  for a prior art InGaAs collector HBT and an InGaAlAs collector HBT for an HBT of the present invention, respectively, of an HBT having a  $3 \times 10 \mu\text{m}^2$  emitter. As is apparent, the prior art HBT in Figure 11(a) has a breakdown voltage of approximately 10v. The HBT of the present invention shown in Figure 11(b) has a breakdown voltage of about 16v.

Figure 12 shows a graph of output conductance  $g_o$  in the vertical axis and collector-emitter voltage  $V_{CE}$  in the horizontal axis that depicts the output conductance of an HBT of the prior art having an InGa/InGaAs collector and an HBT of the present invention having an InGaAlAs collector. As is apparent, the output conductance  $g_o$  is significantly less for the HBT of the present invention as opposed to the prior art HBT.

The foregoing discussion discloses and describes merely exemplary embodiments of the present inven-

tion. One skilled in the art will readily recognize from such discussion, and from the accompanying drawings and claims, that various changes, modifications and variations can be made therein without departing from the spirit and scope of the invention as defined in the following claims.

## Claims

### 1. A semiconductor device comprising:

a substrate;  
an InGaAlAs collector layer positioned on the substrate;  
a base layer positioned on the collector layer;  
a graded InGaAlAs emitter-base transition layer positioned on the base layer; and  
an emitter layer positioned on the emitter-base transition layer, wherein the graded transition layer has a graded composition so as to have substantially the same composition as the base layer at a surface adjacent to the base layer and substantially the same composition as the emitter layer at a surface adjacent to the emitter layer.

### 2. The semiconductor device according to Claim 1 wherein the collector layer has a substantially constant composition of InGaAlAs.

### 3. The semiconductor device according to Claim 2 further comprising a first graded InGaAlAs layer positioned between the collector layer and the substrate and a second InGaAlAs graded layer positioned between the collector layer and the base layer, wherein the first and second graded layers include graded aluminum such that the concentration of aluminum of the first graded layer at a surface of the first graded layer opposite to the collector layer is substantially zero and the concentration of aluminum of the first graded layer at a surface of the first graded layer adjacent to the collector layer is substantially the same as the concentration of aluminum in the collector layer, and wherein the concentration of aluminum of the second graded layer at a surface of the second graded layer adjacent to the base layer is substantially zero and the concentration of aluminum of the second graded layer at a surface of the second graded layer adjacent to the collector layer is substantially the same as the concentration of aluminum in the collector layer.

### 4. The semiconductor device according to Claim 3 wherein the collector layer is approximately 600 nm thick and has an n-type doping of approximately $9 \times 10^{15}$ atoms/cm<sup>3</sup> and the first and second graded layers are approximately 100 nm thick and have an n-type doping of approximately $1 \times 10^{19}$ atoms/cm<sup>3</sup>.

### 5. The semiconductor device according to Claim 1 wherein the base layer is a graded doping base layer including a p-type doping profile having a first doping concentration at a surface of the base layer closest to the collector layer and a second doping concentration at a surface of the base layer proximate to the emitter-base transition layer, wherein the base layer doping profile is graded so that the first doping concentration is less than the second doping concentration.

### 6. The semiconductor device according to Claim 5 wherein the first doping concentration is approximately $1 \times 10^{19}$ atoms/cm<sup>3</sup> and the second doping concentration is approximately $4 \times 10^{19}$ atoms/cm<sup>3</sup>.

### 7. The semiconductor device according to Claim 5 wherein the base layer is an InGaAs base layer.

### 8. The semiconductor device according to Claim 5 wherein the base layer is an InGaAlAs base layer.

### 9. The semiconductor device according to Claim 8 wherein the base layer is a graded-composition InGaAlAs base layer such that the composition of the base layer is substantially the same as the emitter-base transition layer at an interface between the base layer and the transition layer and the composition of the base layer is substantially the same as the collector layer at an interface between the base layer and the collector layer.

### 10. The semiconductor device according to Claim 1 wherein the base layer is a graded-composition InGaAlAs base layer such that the composition of the base layer is substantially the same as the emitter-base transition layer at an interface between the base layer and the transition layer and the composition of the base layer is substantially the same as the collector layer at an interface between the base layer and the collector layer.

### 11. The semiconductor device according to Claim 1 wherein the emitter layer is an InAlAs emitter layer.

### 12. The semiconductor device according to Claim 1 wherein the substrate is an InP substrate.

### 13. The semiconductor device according to Claim 1 wherein the emitter-base transition layer has an n-type doping concentration of approximately $5 \times 10^{17}$ atoms/cm<sup>3</sup> and a thickness of approximately 45 nm.

### 14. A semiconductor device comprising:

a substrate;  
an InGaAlAs collector layer positioned on the substrate; and

a base layer positioned on the collector layer, said base layer including a p-type graded doping profile having a first doping concentration at a surface of the base layer closest to the collector layer and a second doping concentration at a surface of the base layer proximate to an emitter layer, wherein the base layer doping profile is graded so that the first doping concentration is less than the second doping concentration.

15. The semiconductor device according to Claim 14 wherein the base layer is an InGaAs base layer.

16. The semiconductor device according to Claim 14 wherein the base layer is an InGaAlAs base layer.

17. The semiconductor device according to Claim 16 wherein the InGaAlAs base layer has a graded composition such that the concentration of Ga and Al closest to the collector layer is the highest and the concentration of Ga and Al closest to the emitter layer is the lowest.

18. The semiconductor device according to Claim 14 wherein the collector layer has a substantially constant composition of InGaAlAs, said semiconductor device, said semiconductor device further comprising a first graded InGaAlAs layer positioned between the collector layer and the substrate and a second InGaAlAs graded layer positioned between the collector layer and the base layer, wherein the first and second graded layers include graded aluminum such that the concentration of aluminum of the first graded layer at a surface of the first graded layer opposite to the collector layer is substantially zero and the concentration of aluminum of the first graded layer at a surface of the first graded layer adjacent to the collector layer is substantially the same as the concentration of aluminum in the collector layer, and wherein the concentration of aluminum of the second graded layer at a surface of the second graded layer adjacent to the base layer is substantially zero and the concentration of aluminum of the second graded layer at a surface of the second graded layer adjacent to the collector layer is substantially the same as the concentration of aluminum in the collector layer.

19. A semiconductor device comprising:

a substrate;

an InGaAlAs collector layer positioned on the substrate;

an InGaAlAs base layer positioned on the collector layer; and

an InAlAs emitter layer positioned on the base layer, wherein the base layer is a graded composition base layer such that the base layer has

its highest composition of Ga and Al proximate to the collector layer and has its least amount of Ga and Al closest to the emitter layer.

20. The semiconductor device according to Claim 19 wherein the collector layer has a substantially constant composition of InGaAlAs, said semiconductor device further comprising a first graded InGaAlAs layer positioned between the collector layer and the substrate and a second InGaAlAs graded layer positioned between the collector layer and the base layer, wherein the first and second graded layers include graded aluminum such that the concentration of aluminum of the first graded layer at a surface of the first graded layer opposite to the collector layer is substantially zero and the concentration of aluminum of the first graded layer at a surface of the first graded layer adjacent to the collector layer is substantially the same as the concentration of aluminum in the collector layer, and wherein the concentration of aluminum of the second graded layer at a surface of the second graded layer adjacent to the base layer is substantially zero and the concentration of aluminum of the second graded layer at a surface of the second graded layer adjacent to the collector layer is substantially the same as the concentration of aluminum in the collector layer.



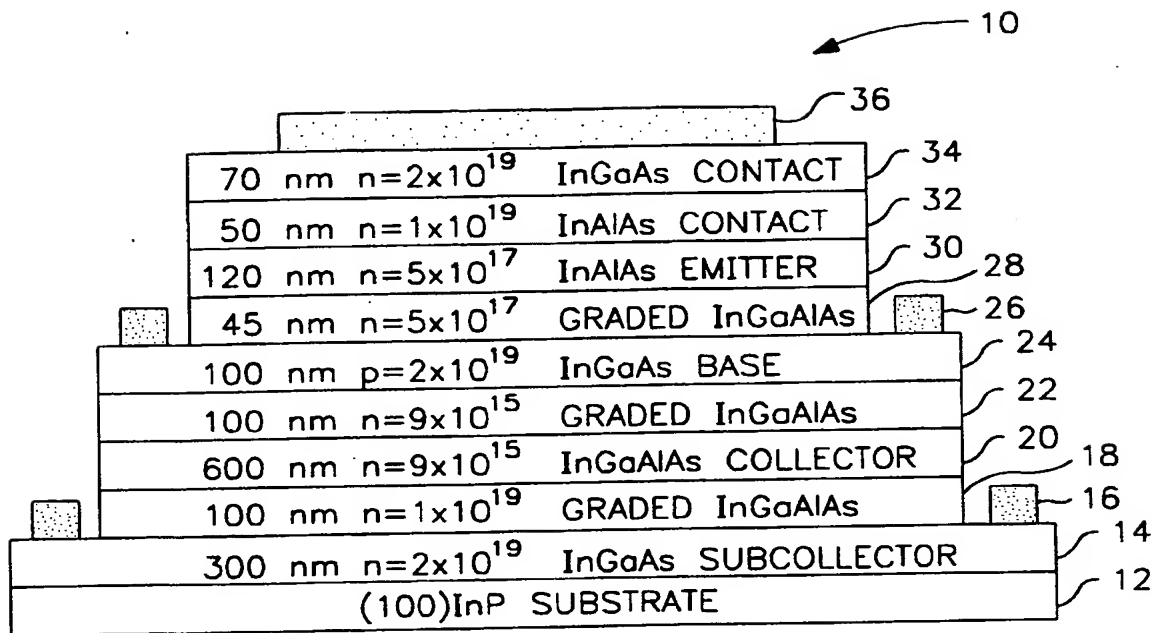


FIG. 1

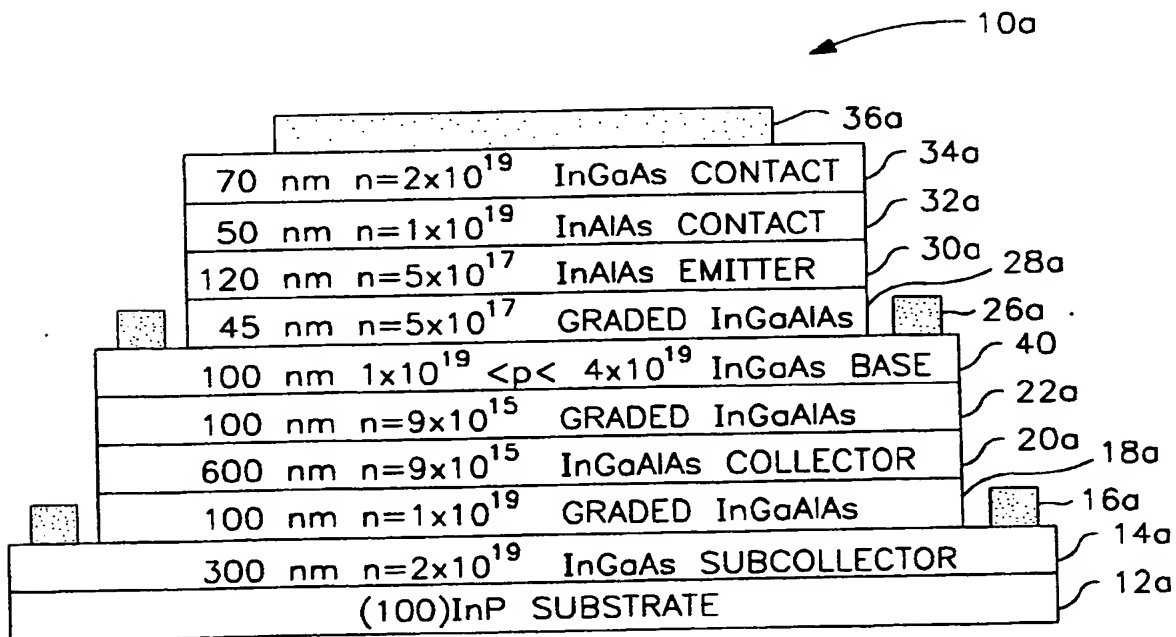


FIG. 2



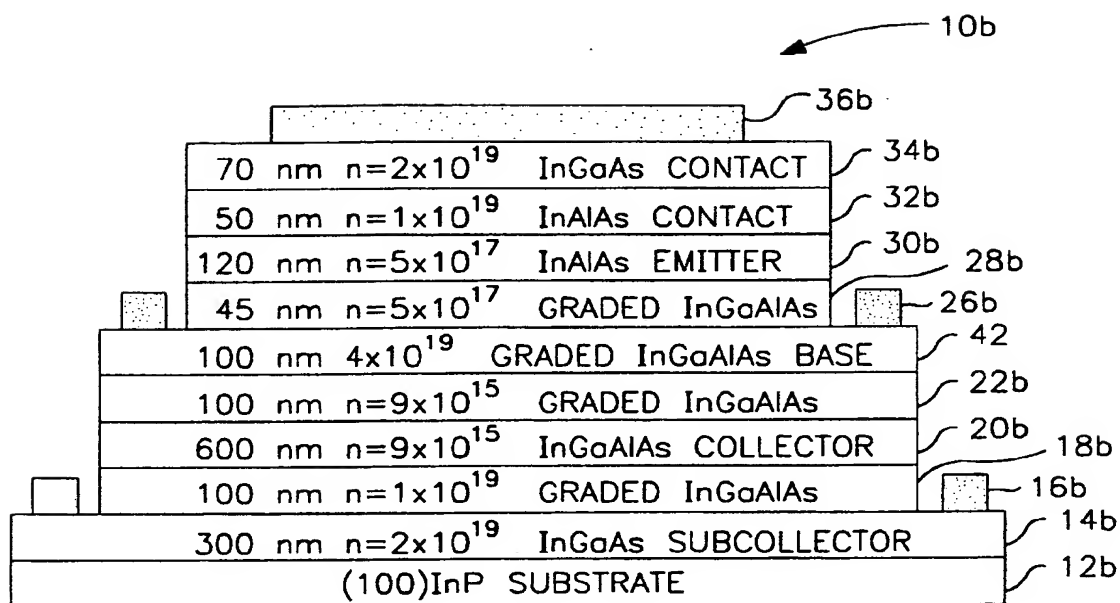


FIG. 3

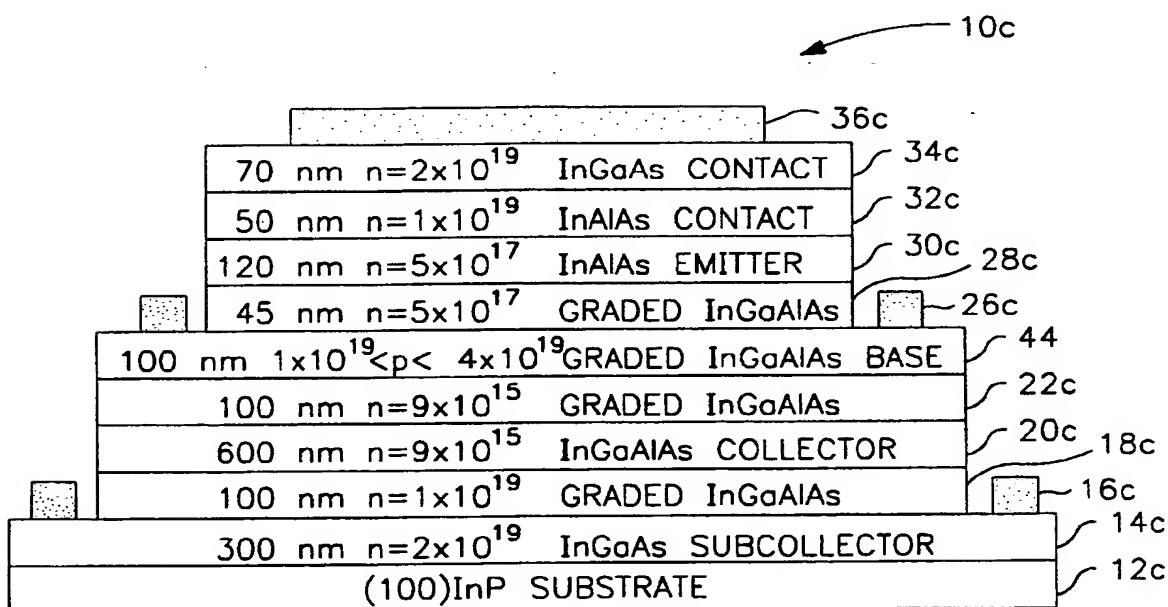


FIG. 4

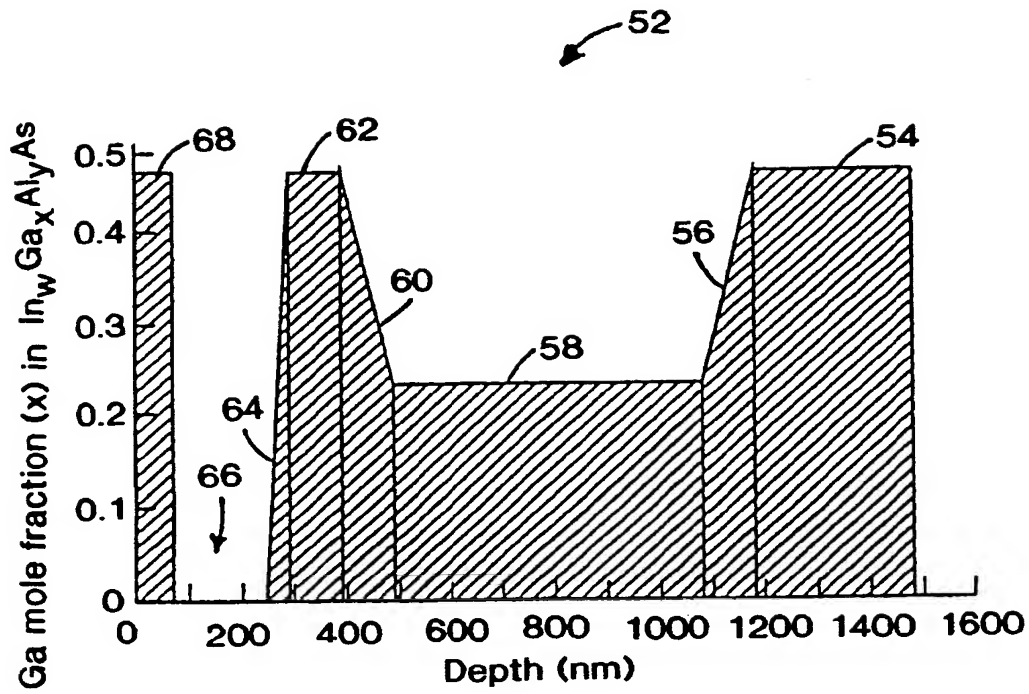


FIG. 5

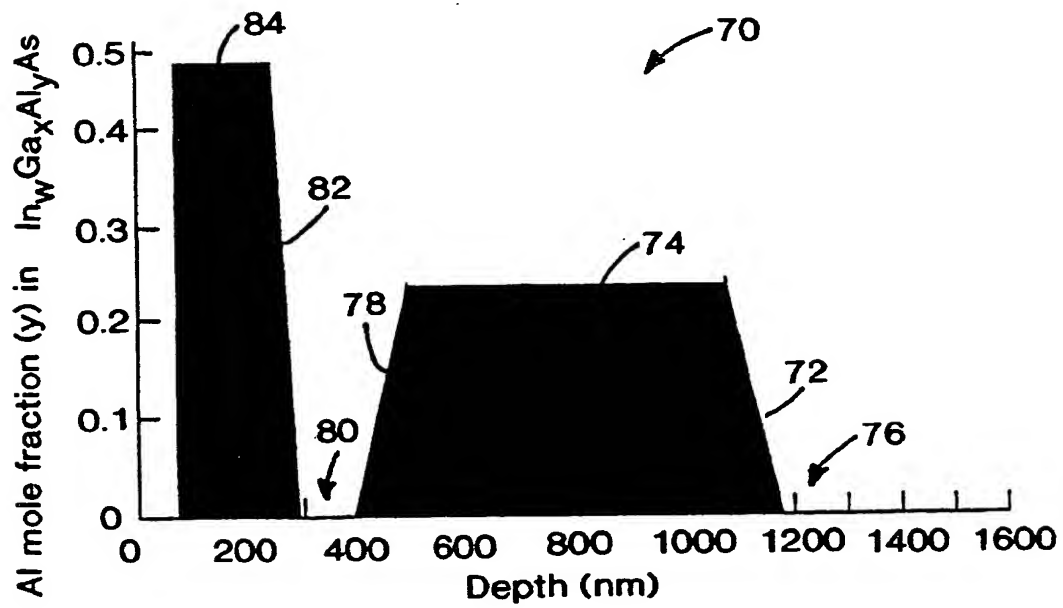


FIG. 6

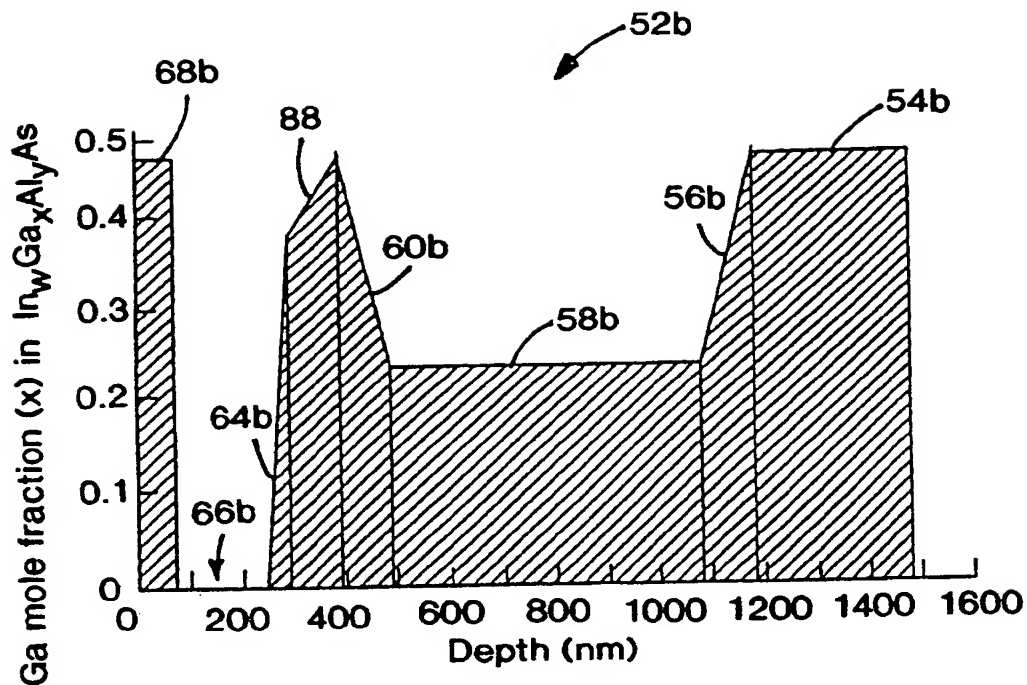


FIG. 7

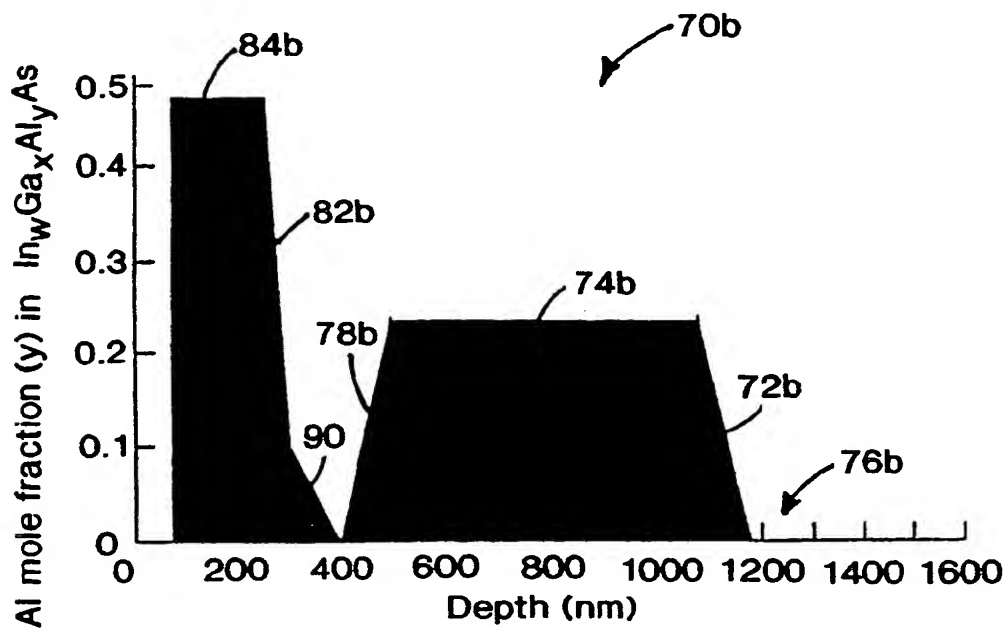


FIG. 8

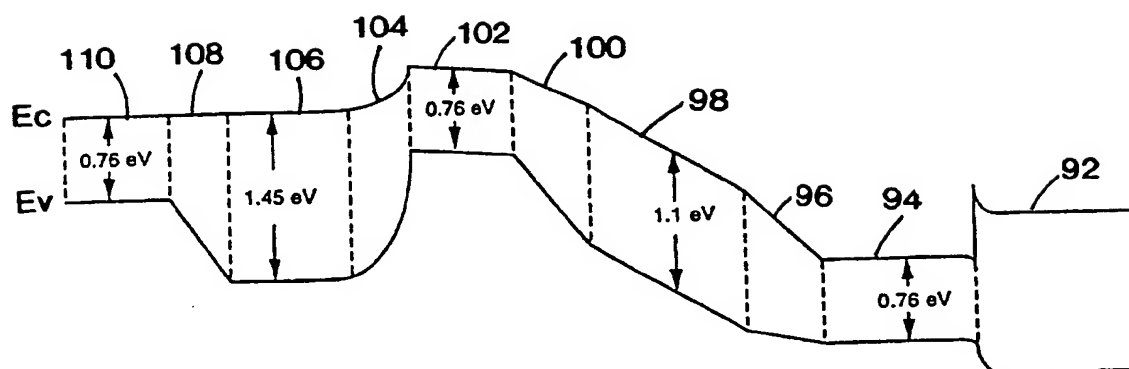


FIG. 9

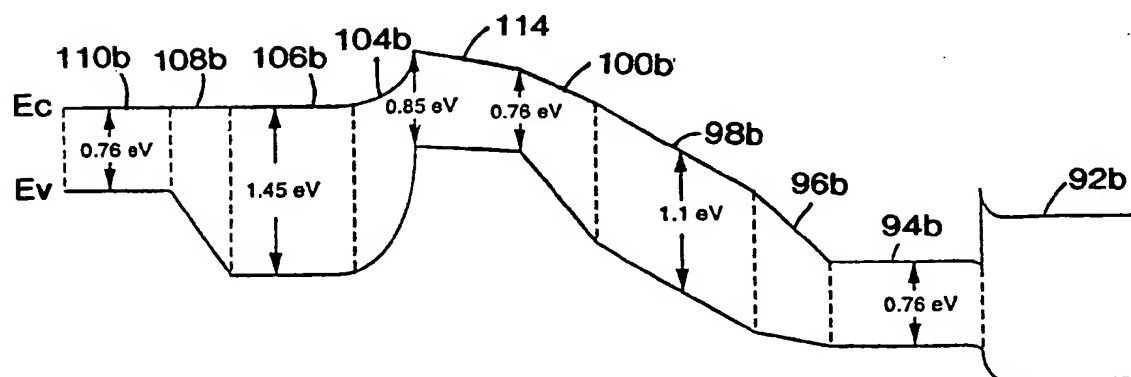


FIG. 10

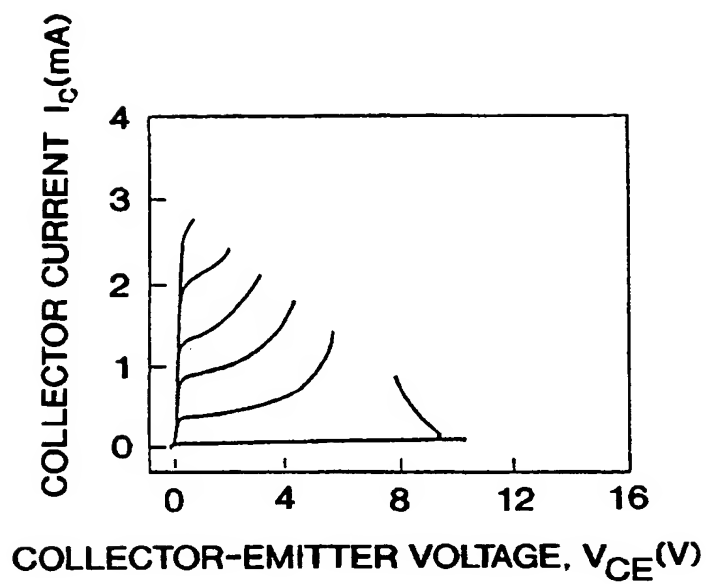


FIG. 11A

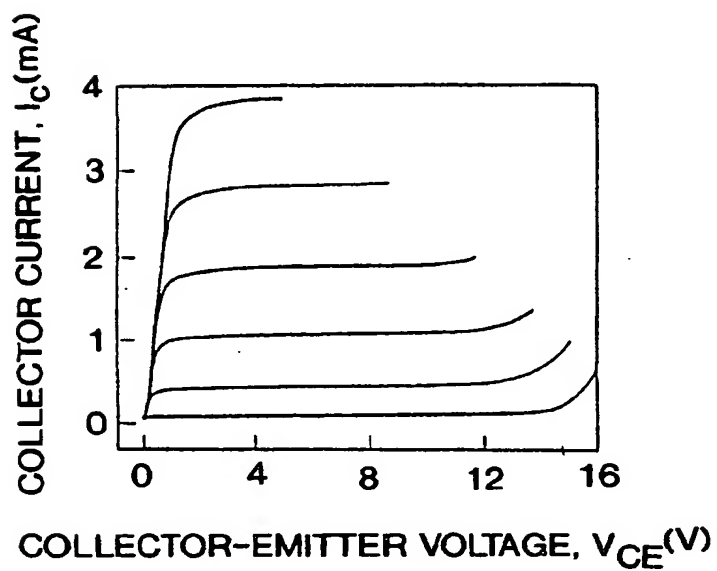


FIG. 11B

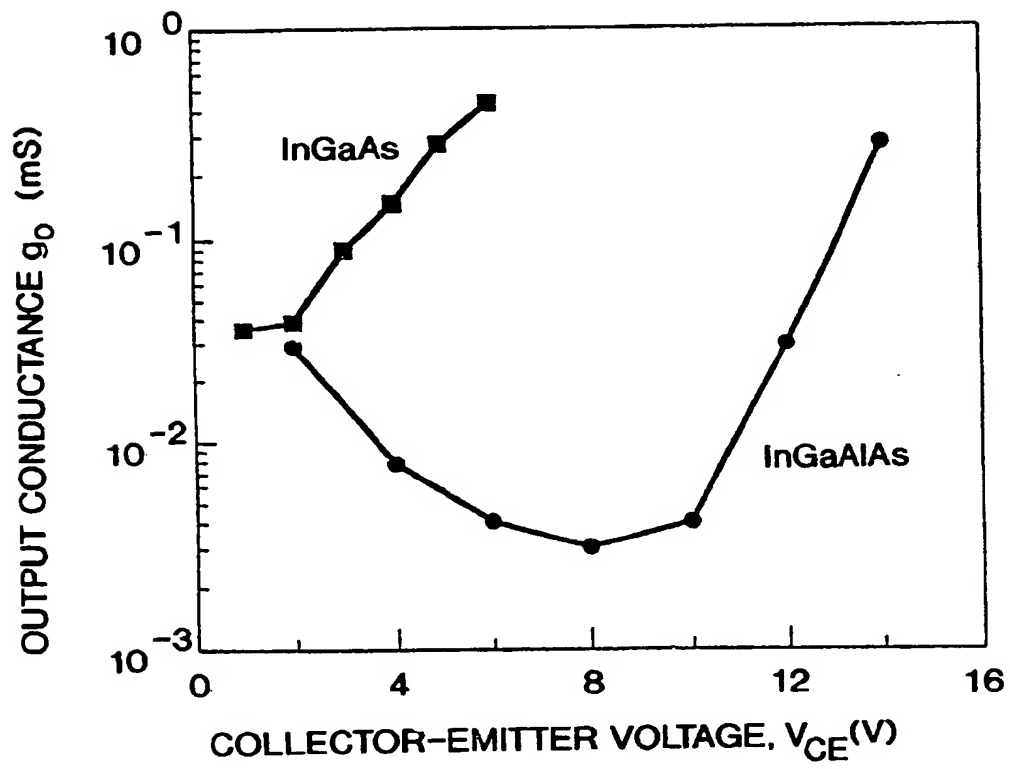
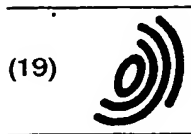


FIG. 12



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(54) Quarternary collector InAlAs-InGaAlAs heterojunction bipolar transistor

(57) An InAlAs/InGaAlAs heterojunction bipolar transistor that includes a constant quarternary InGaAlAs collector layer. Graded InGaAlAs collector layers are provided on each side of the quarternary collector layer to minimize transitions through the constant collector

layer. The InAlAs/InGaAlAs HBT may also include one or more of a graded InGaAlAs emitter-base transition region, a graded-doping InGaAs base layer, and a graded-composition InGaAlAs base layer.

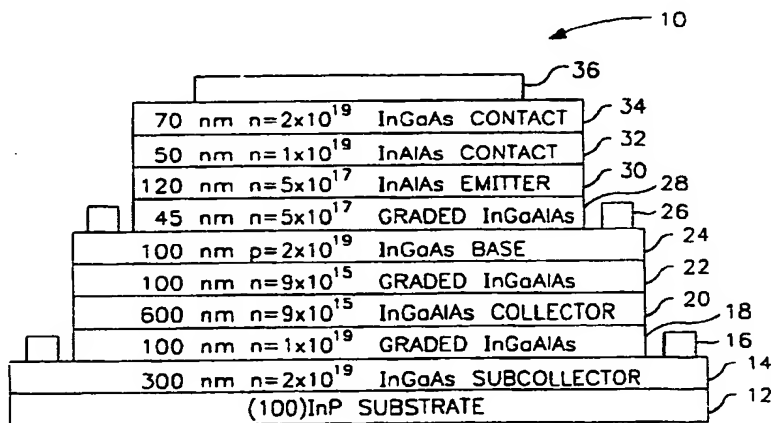


FIG. 1

EP 0 746 035 A3





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# EUROPEAN SEARCH REPORT

Application Number  
EP 96 10 6375

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
D,X	VLCEK J C ET AL: "MULTIPLY-GRADED INGAALAS HETEROJUNCTION BIPOLAR TRANSISTORS" ELECTRONICS LETTERS, vol. 27, no. 13, 20 June 1991, pages 1213-1215, XP000238769	1,2,10, 14-16,19	H01L29/08 H01L29/737
Y	* page 1214, column 1, paragraph 1; figure 1 *	5,7-9	
A		3,4,6, 11,12, 18,20	
X	US 4 958 208 A (TANAKA SHIN-ICHI) 18 September 1990	1,10-13, 19	
A	* column 4, line 53 - column 6, line 26; claim 25; figures 5,6 *	20	
X	US 5 010 382 A (KATOH RIICHI) 23 April 1991	1,10-12, 19	
A	* figure 12 *	3,4,20	
Y	US 5 371 389 A (MATSUNO TOSHINOBU ET AL)	5,7-9	TECHNICAL FIELDS SEARCHED (Int.Cl.6)
A	* column 4, line 1 - line 22; claim 1 *	6,7,15, 17	H01L
A	WU Y H ET AL: "A NEW INP-BASED HETEROJUNCTION BIPOLAR TRANSISTOR UTILIZING AN IN0.53AL0.22GA0.25AS BASE" APPLIED PHYSICS LETTERS, vol. 66, no. 3, 16 January 1995, page 347/348 XP000486044	2	
D,A	US 5 150 185 A (YAMADA HIROSHI) 22 September 1992	3,18,20	
	* figure 5 *		
The present search report has been drawn up for all claims			
Place of search		Date of completion of the search	Examiner
BERLIN		15 December 1997	Juhl, A
CATEGORY OF CITED DOCUMENTS			
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Y : particularly relevant if combined with another document of the same category		E : earlier patent document, but published on, or after the filing date	
A : technological background		D : document cited in the application	
O : non-written disclosure		L : document cited for other reasons	
P : intermediate document		& : member of the same patent family, corresponding document	

EPD FORM 1503 03 82 (P04C01)



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# EUROPEAN SEARCH REPORT

Application Number  
EP 96 10 6375

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
A	EP 0 366 552 A (FUJITSU LTD)	9,17	
D	* figure 8 * & US 5 284 783 A -----		
			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
The present search report has been drawn up for all claims			
Place of search		Date of completion of the search	Examiner
BERLIN		15 December 1997	Juhl, A
<p><b>CATEGORY OF CITED DOCUMENTS</b></p> <p>X : particularly relevant if taken alone  Y : particularly relevant if combined with another document of the same category  A : technological background  O : non-written disclosure  P : intermediate document</p> <p>T : theory or principle underlying the invention  E : earlier patent document, but published on, or after the filing date  D : document cited in the application  L : document cited for other reasons  &amp; : member of the same patent family, corresponding document</p>			

EPO FORM 1503 03/82 (P04C01)



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Application Number  
EP 96 10 6375

**CLAIMS INCURRING FEES**

The present European patent application comprised at the time of filing more than ten claims.

- ☐ Only part of the claims have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims and for those claims for which claims fees have been paid, namely claim(s):
- ☐ No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims.

**LACK OF UNITY OF INVENTION**

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

**SEE SHEET B**  
**(in case of Lack of Unity)**

- ☒ All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.
- ☐ Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid, namely claims:
- ☐ None of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims, namely claims:



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### CLAIMS INCURRING FEES

The present European patent application comprised at the time of filing more than ten claims.

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- ☐ Only part of the claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims and for those claims for which claims fees have been paid, namely claims:
- ☐ No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims.

### LACK OF UNITY OF INVENTION

The Search Division considers that the present European patent application does not comply with the requirement of unity of invention and relates to several inventions or groups of inventions, namely:

1. Claims 1 - 4, 10 - 13:  
Semiconductor device with InGaAlAs collector and InGaAlAs emitter base transition layer.
2. Claims 5 - 9, 14 - 18:  
Semiconductor device with InGaAlAs collector and a base layer having a p-type graded doping profile.
3. Claims 19, 20:  
Semiconductor device with InGaAlAs collector and InGaAlAs base and InAlAs emitter.

- ☒ All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.
- ☐ Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respects of which search fees have been paid, namely claims:
- ☐ None of the further search fees has been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims, namely claims: